



### SPECIFICATION AMENDMENTS

On page 1, line 5: please ensure that the serial number of U.S. provisional patent application “60/344,997” has been properly entered. This was entered via an earlier filed preliminary amendment.

On page 1, starting at line 13: please amend the paragraph as follows:

“Prior art systems have commonly attempted to ~~employs~~ employ on board skewing of clock signals between various devices. These on board delay lines typically are extremely space consumptive. As the premium of on board and chip real estate even continues to grow in terms of importance and cost, these approaches of ~~uses~~ using space on the board to serve as such elements fail to meet the design considerations of space conservation and efficient space utilization.”

On page 3, line 23: please amend “~~combes~~ combinations of frequency and phase”.

On page 9, lines 22-23: please amend “~~combes~~ combinations of frequency and phase that may be selectable using the registers or pins. Alternatively, ~~combes~~ of registers”.

On page 9, lines 16-19 (i.e., paragraph from lines 12-19): please amend the paragraph at this location as follows:

“In addition, a number of clock signals (shown as clock 1, ..., and clock n) and data signals (shown as data 1, ..., and data n) may be provided to the device 470. During the clock cycles, the data may be transported on the rising or falling edge of the clock cycle without departing from the scope and spirit of the invention. The device 410 may

include internal ~~registers 435~~ register(s) 445 and/or external ~~registers~~ register(s) 430. Each of the internal ~~registers 430~~ register(s) 445 and the external ~~registers 435~~ register(s) 430 may be programmable. The internal ~~registers 430~~ register(s) 445 and/or the external ~~registers 435~~ register(s) 430 may be used to select the clock frequency and phase of the clock signal, to be generated by the clock generators 412, the dividers 414, and the delay cells 416 within the device 410.”

On page 10, line 1: please amend “and/or ~~combes~~ combinations of pins”.